

Fig 1 PRIOR ART

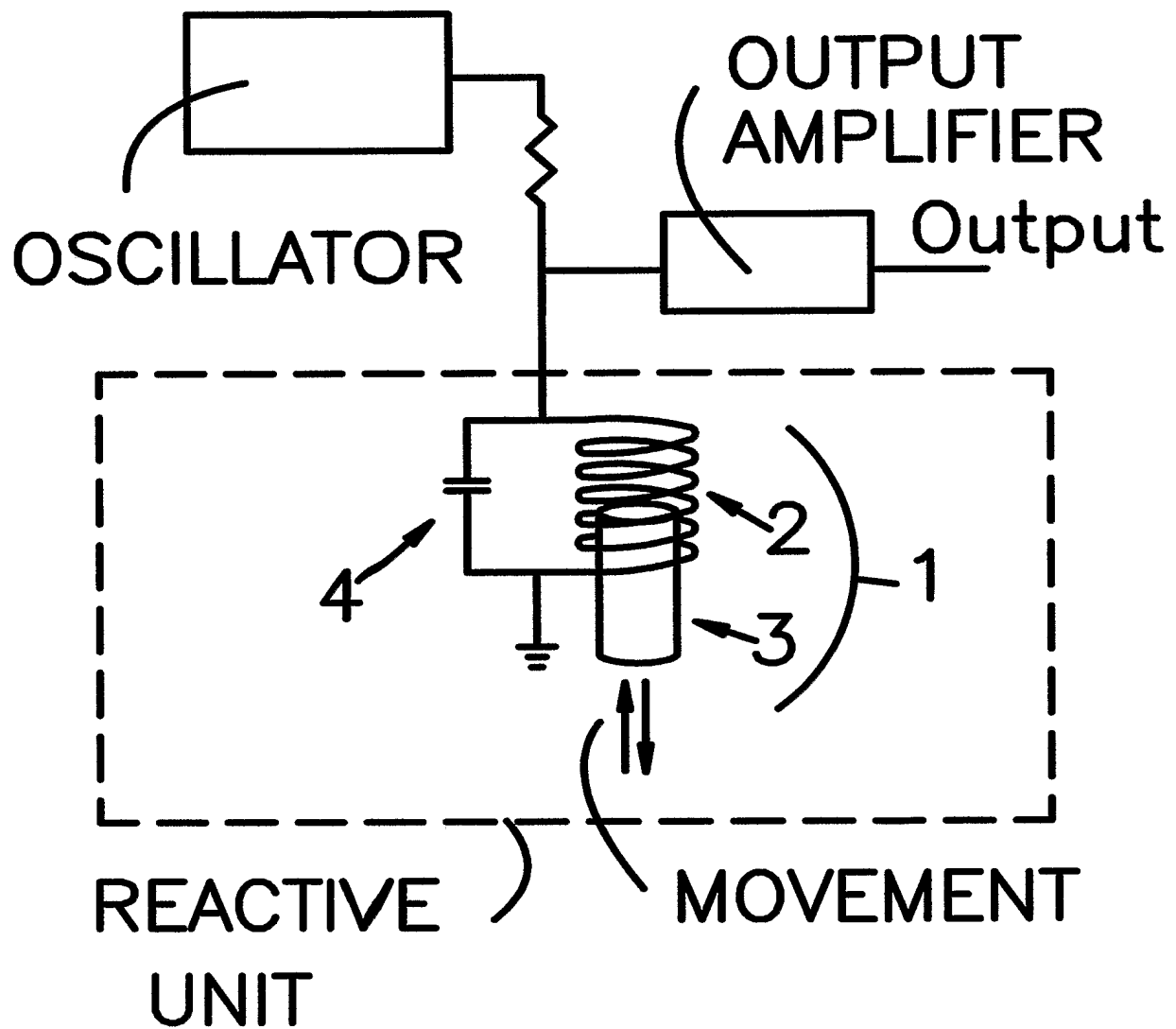
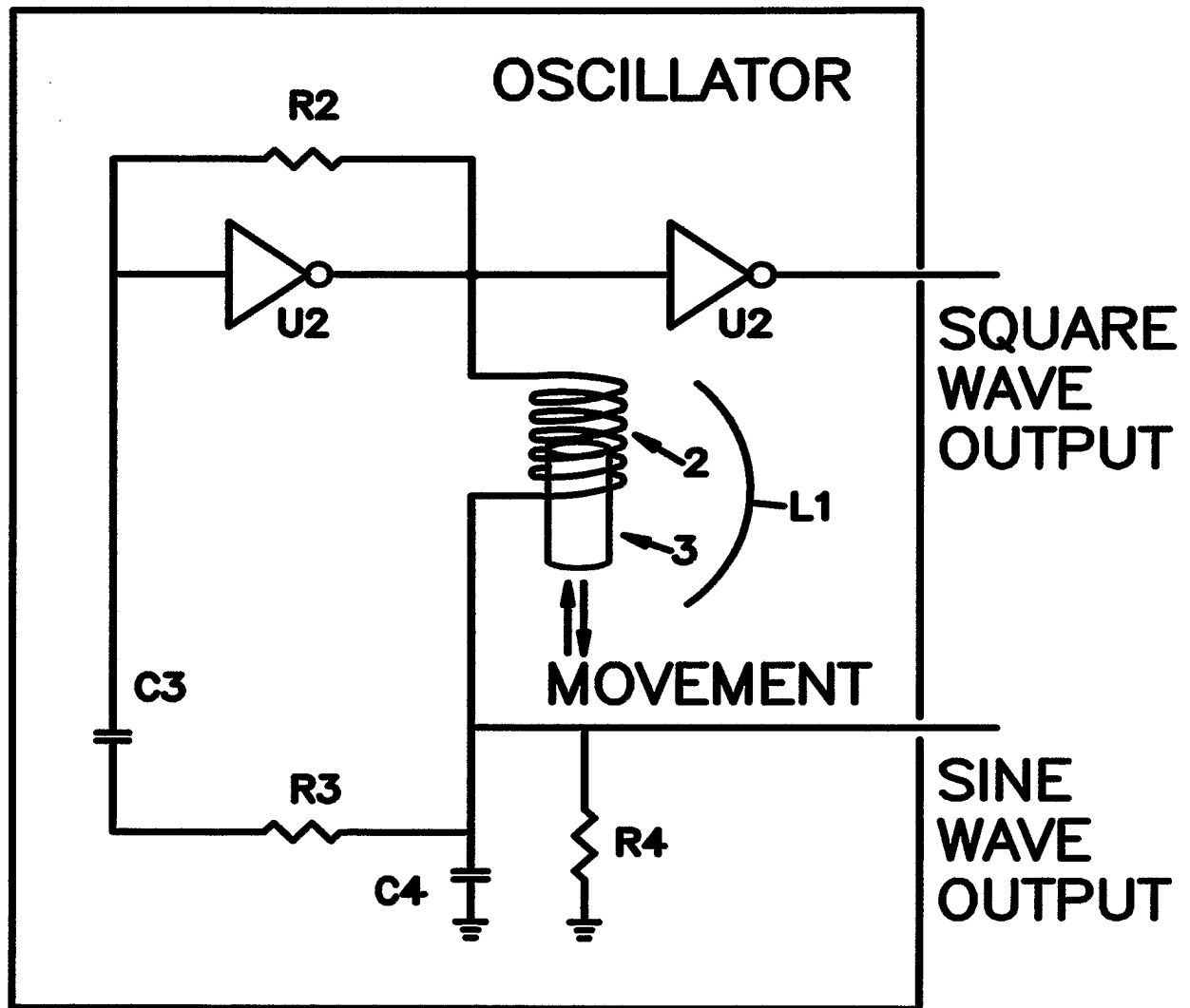


Fig 2



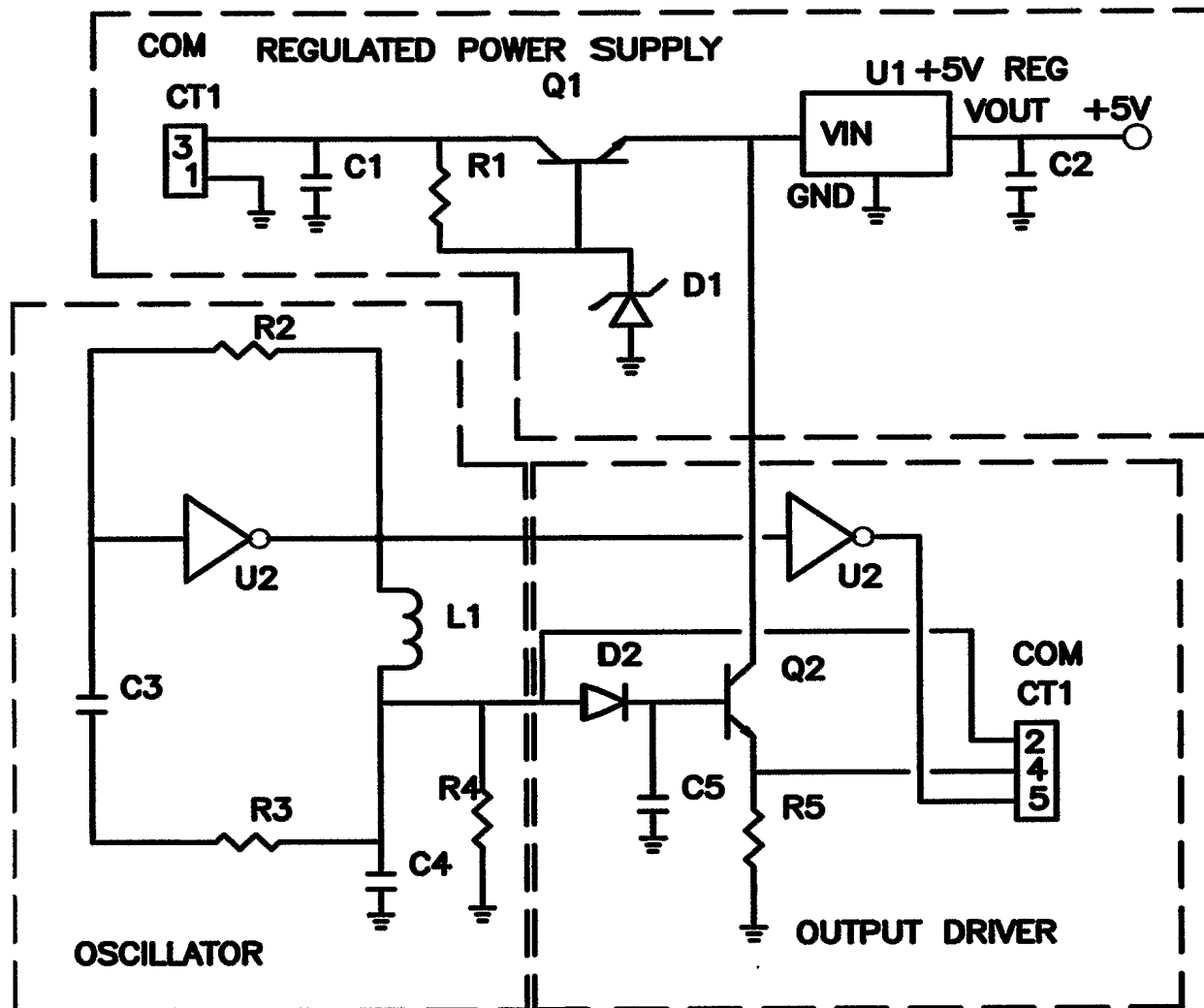
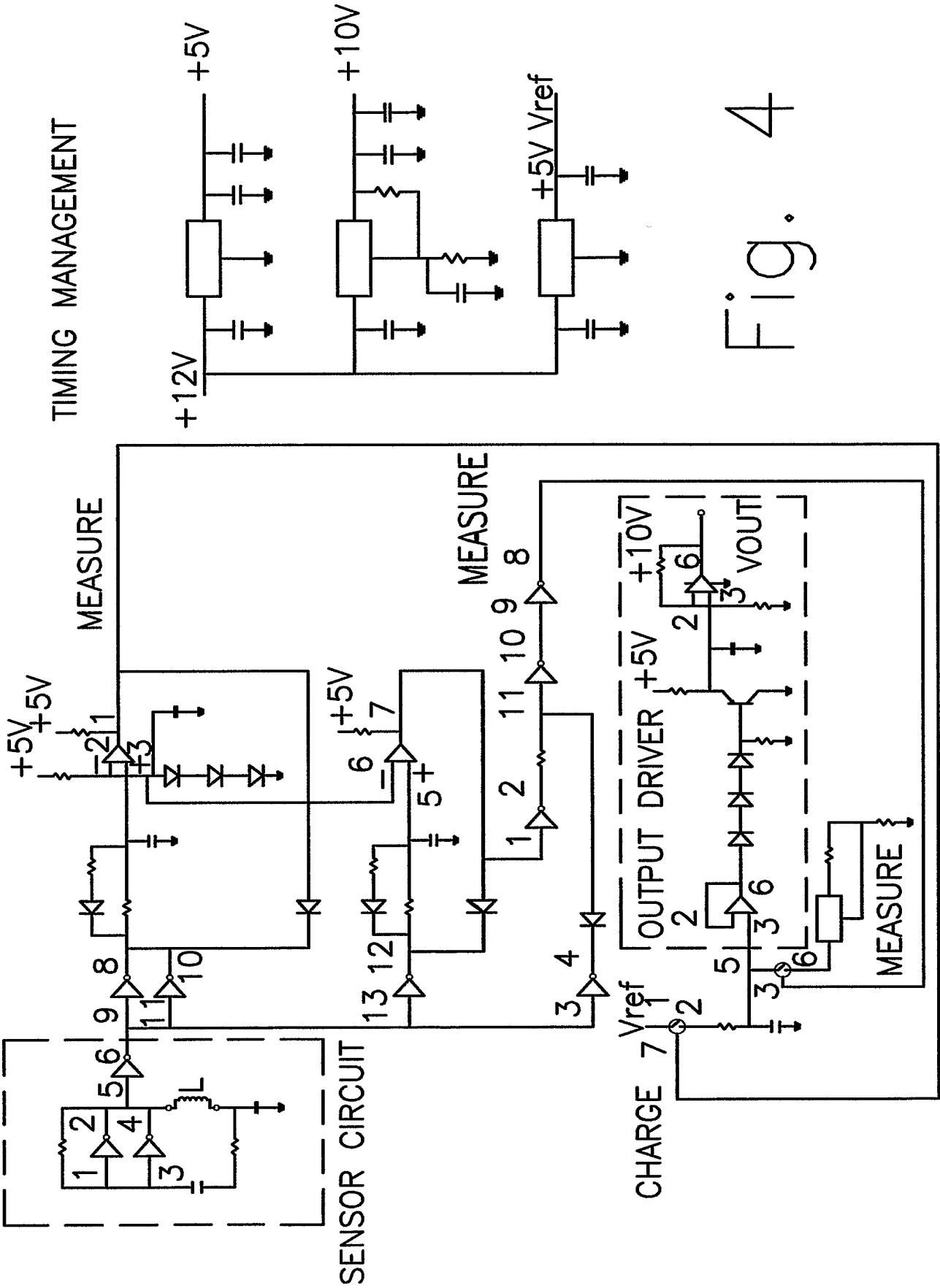


Fig 3



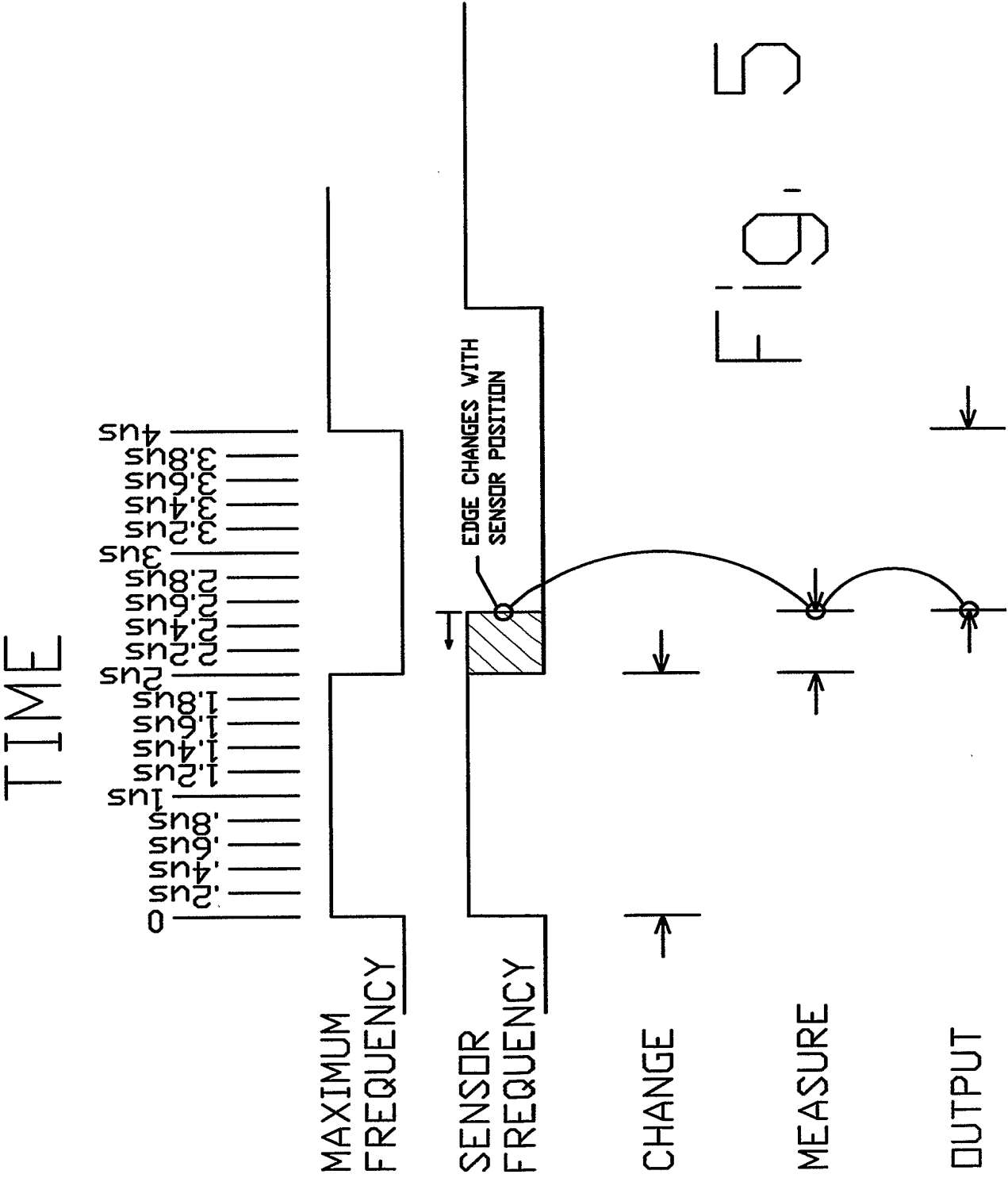


Fig. 5

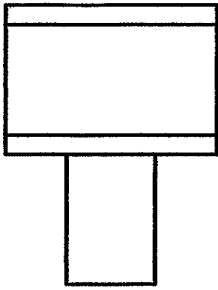


Fig 6a

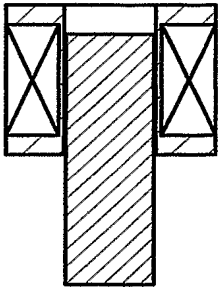


Fig 6c

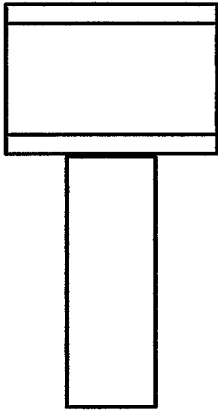
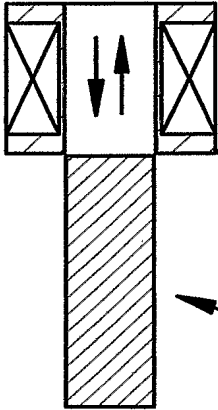


Fig 6b



section AA

Fig 6d

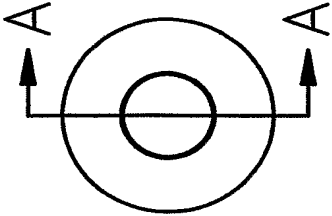


Fig 6e

Fig 6

movement

Fig 7a

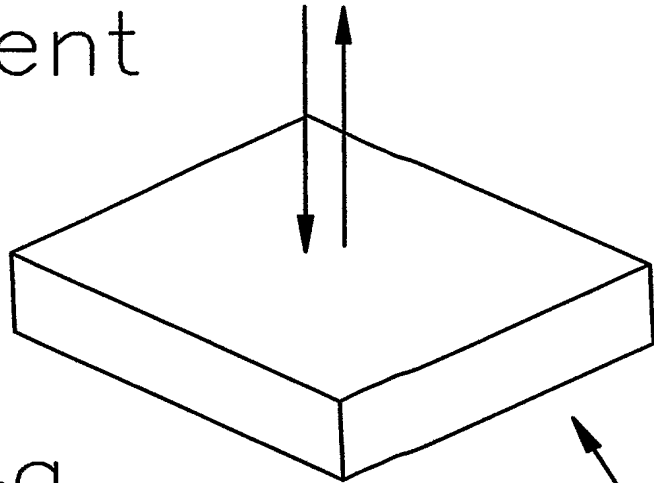


Fig 7b

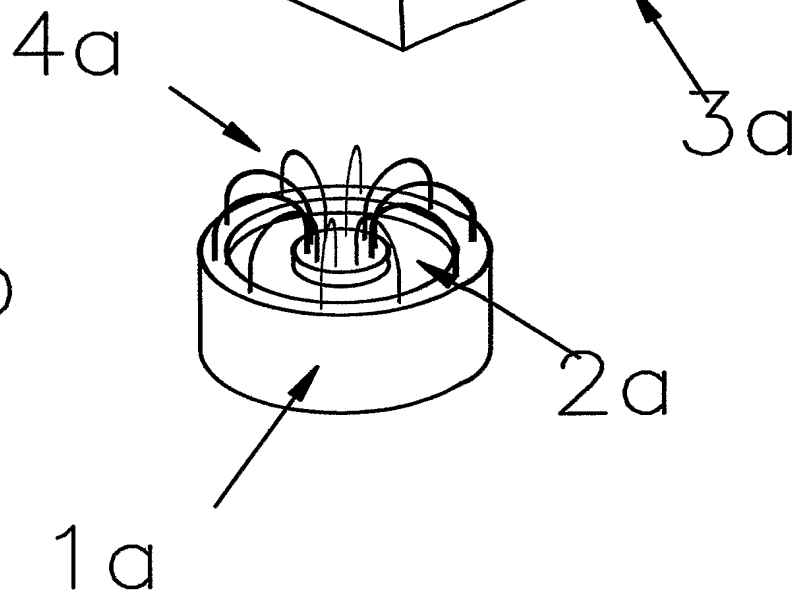


Fig 7c

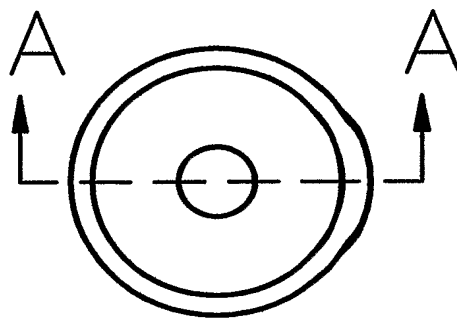


Fig 7d

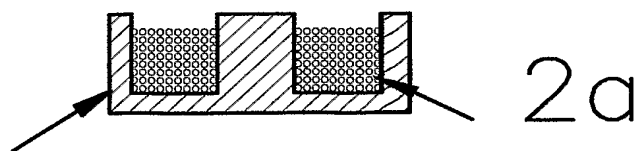


Fig 7 1a section AA

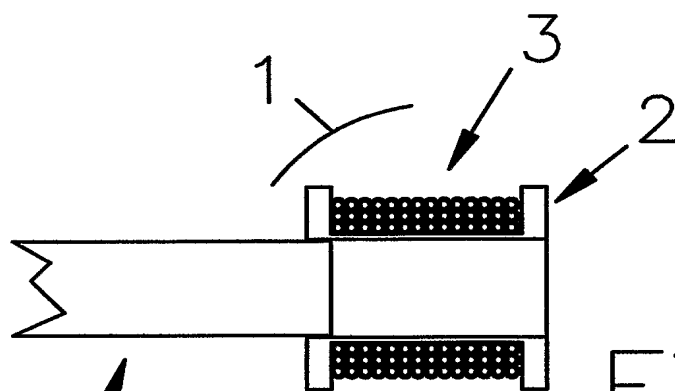


Fig 8a

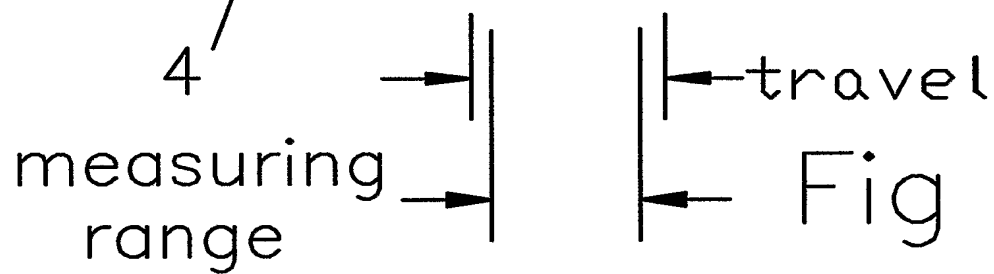


Fig 8b

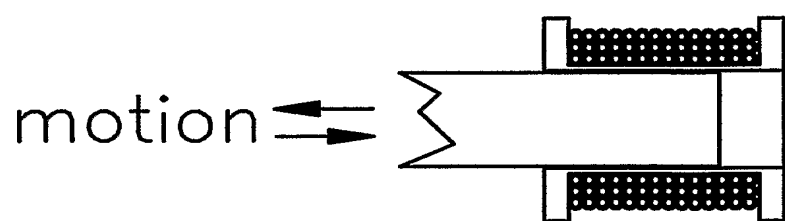


Fig 8c

Fig 8

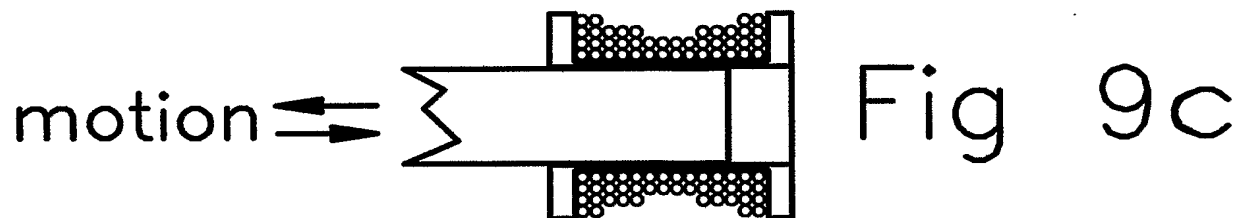
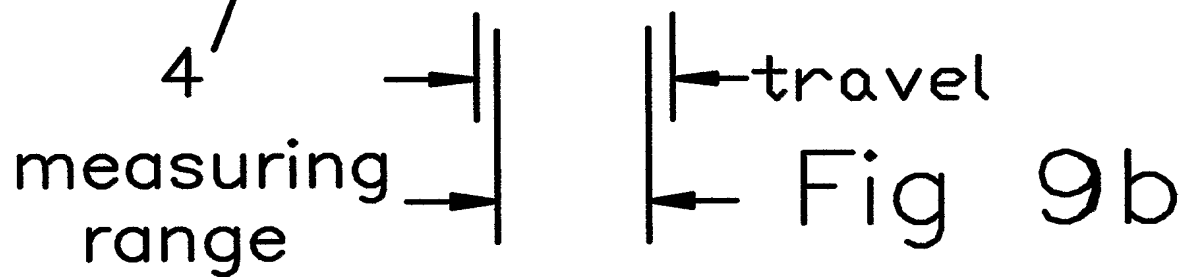
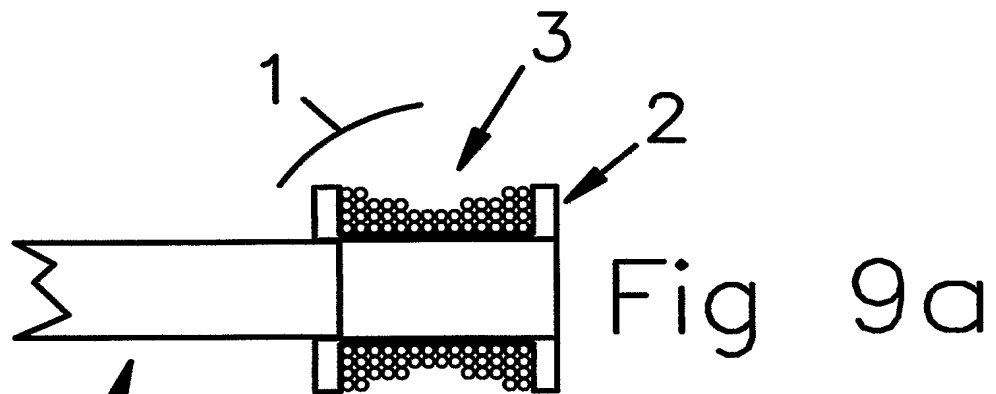


Fig 9

Fig 10c Fig 10d

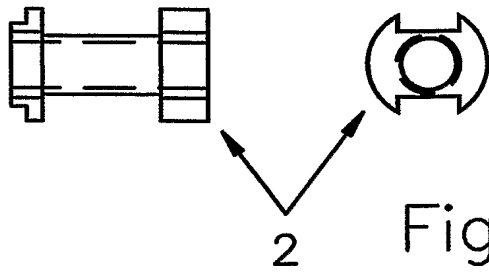


Fig 10e Fig 10f

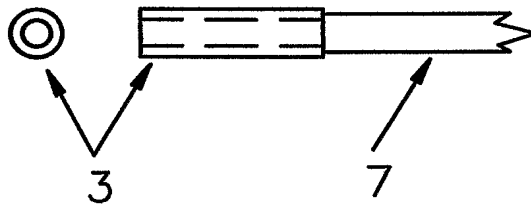


Fig 10g Fig 10h

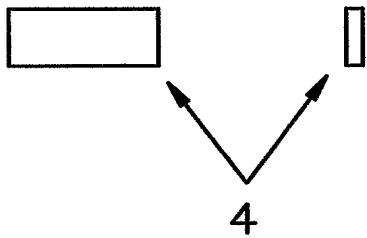
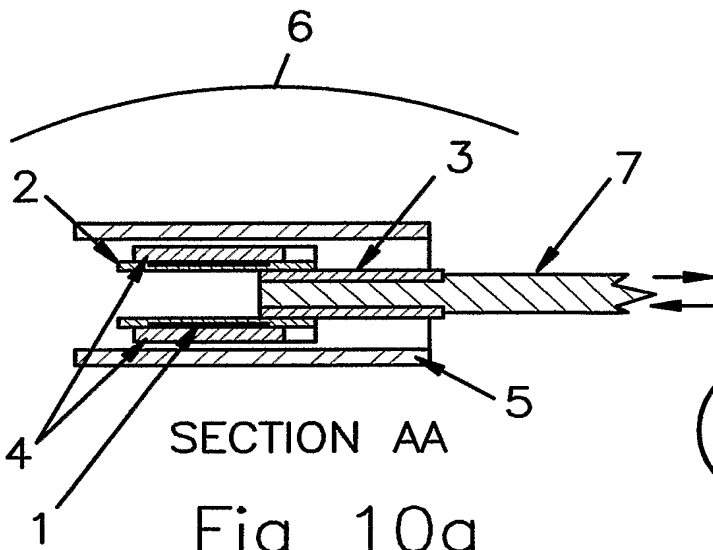
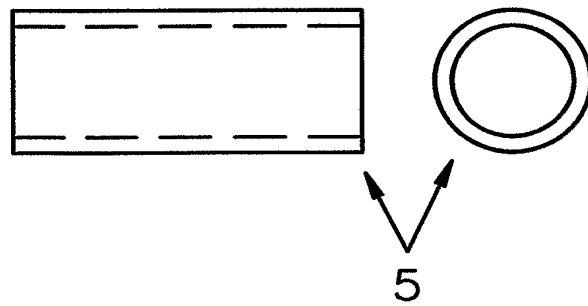


Fig 10i Fig 10j



SECTION AA

Fig 10a

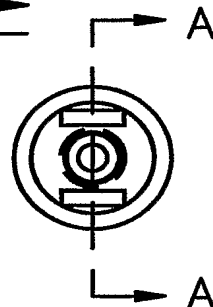


Fig 10b

Fig 10

Fig 11

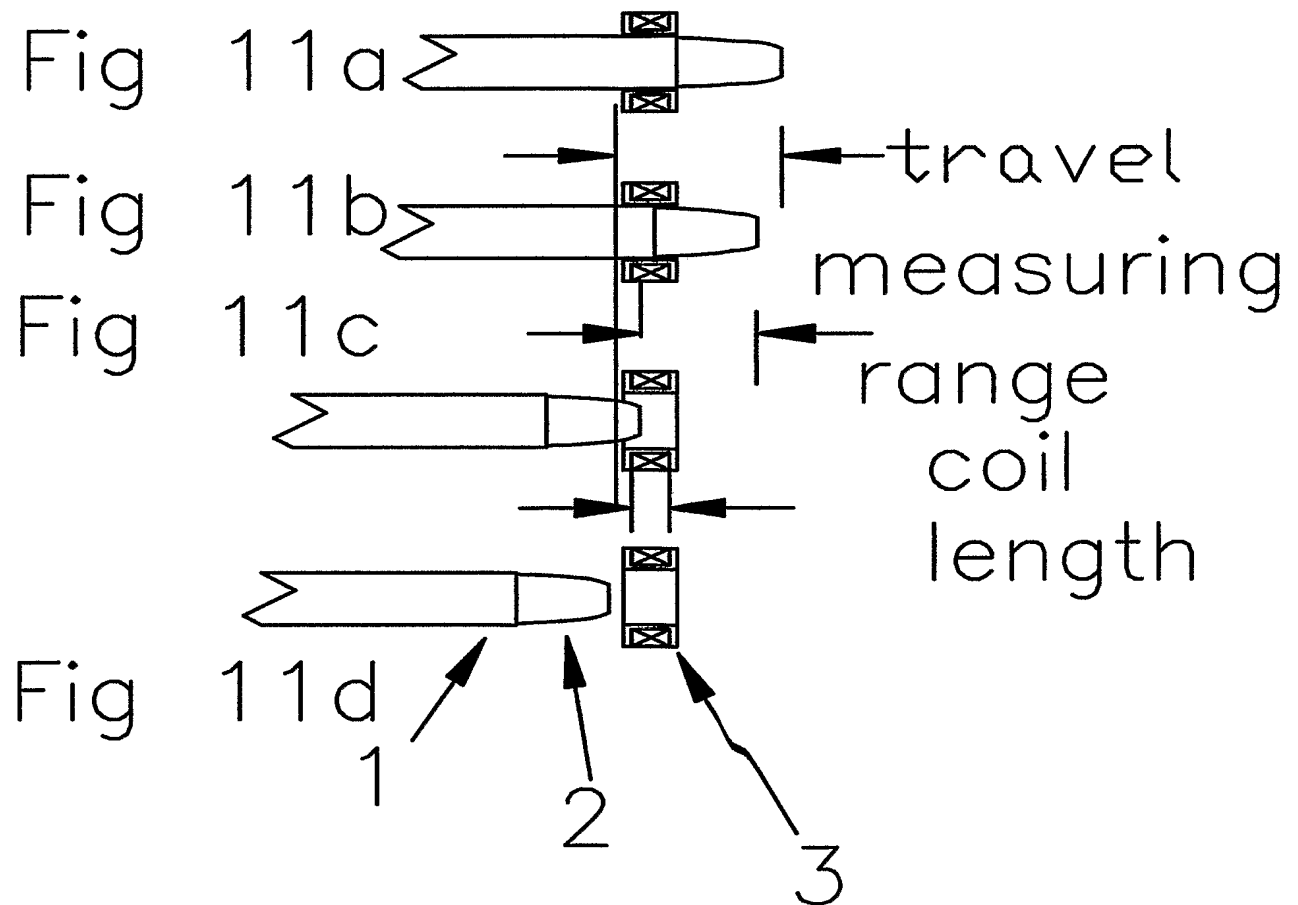


Fig 12

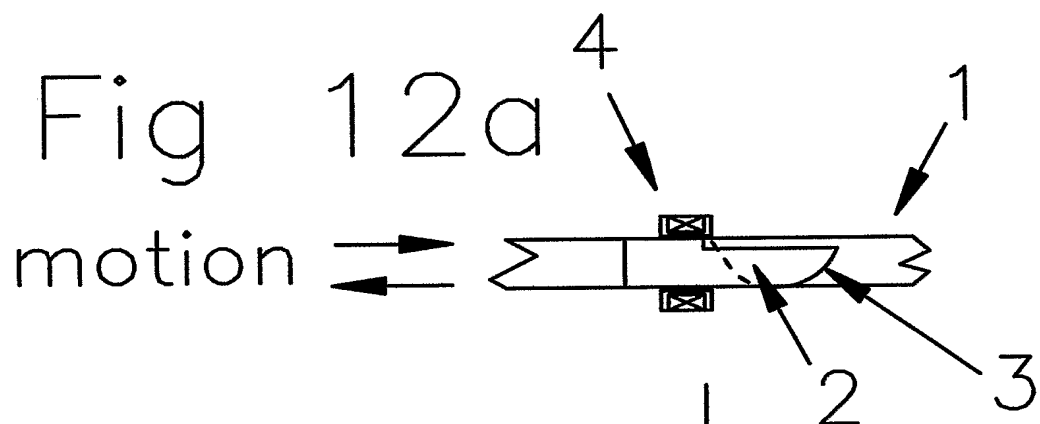


Fig 12b

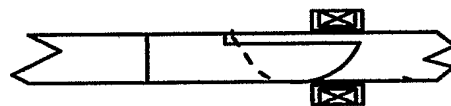


Fig 12c

Fig 13

Fig 13a

Fig 13b

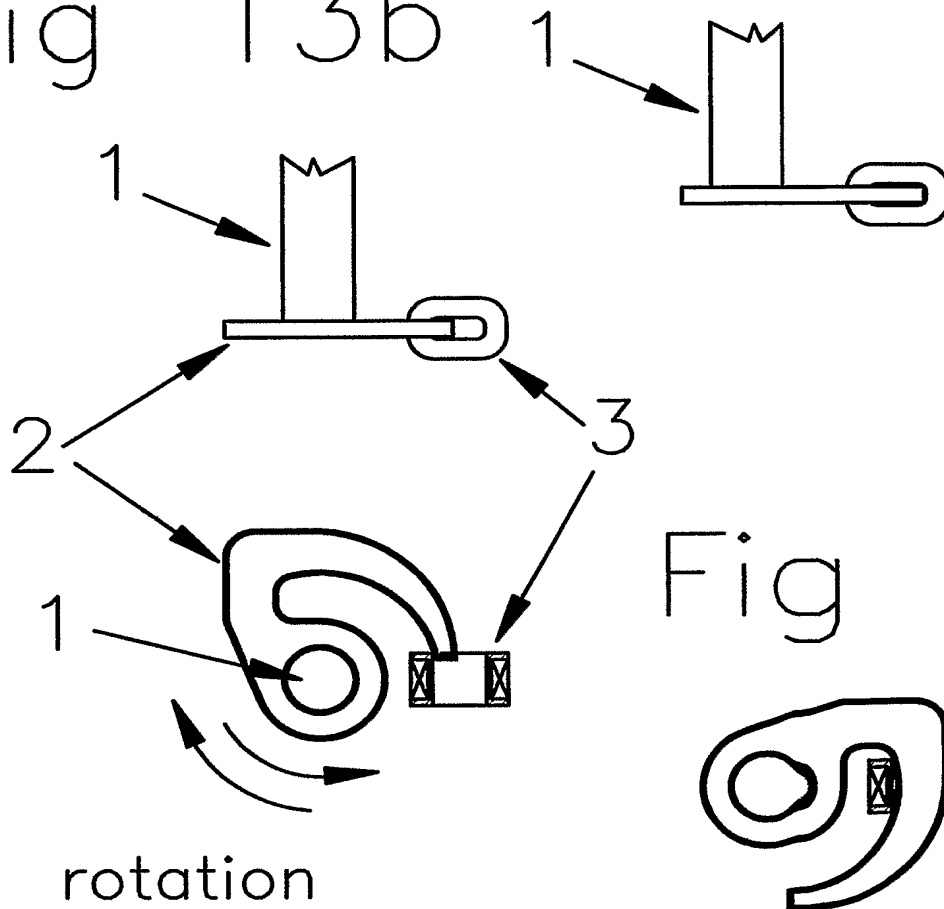


Fig 13c

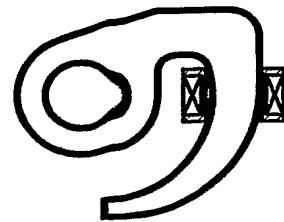


Fig 13d

Fig 14a

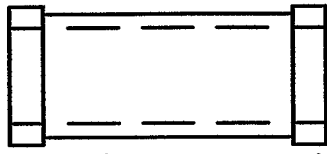
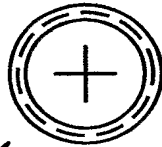


Fig 14b



2

Fig 14c

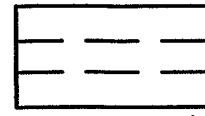
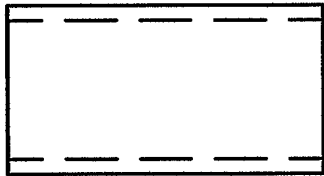


Fig 14d



3

Fig 14e



4

Fig 14f

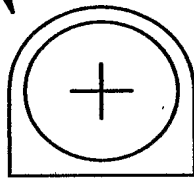
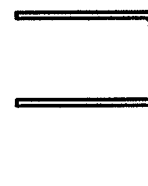


Fig 14g



5

Fig 14h



Fig 14i

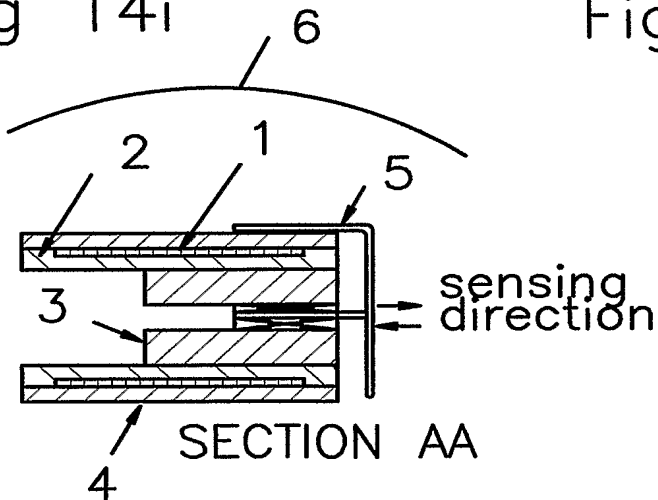


Fig 14j

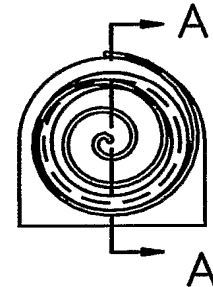


Fig 14

Fig 15a

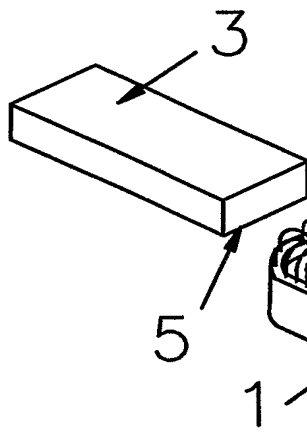


Fig 15b

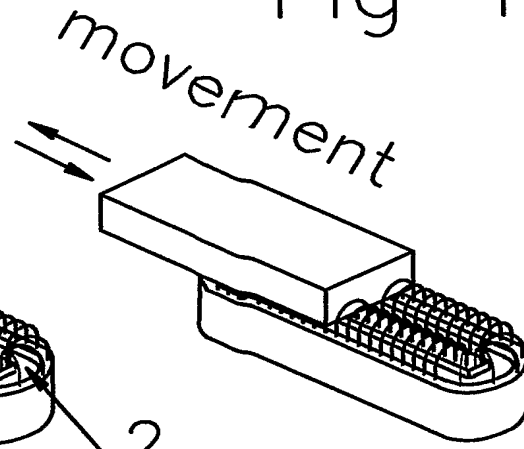
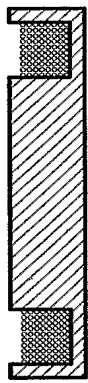


Fig 15c



section DD

Fig 15d

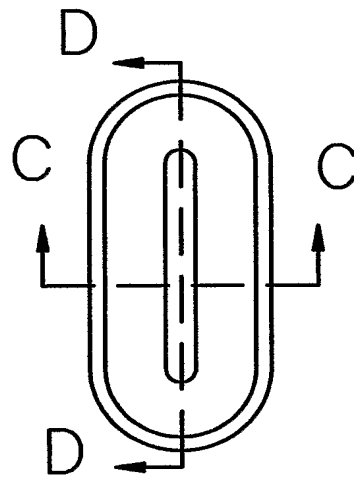
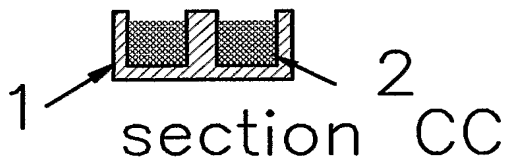


Fig 15f



section CC

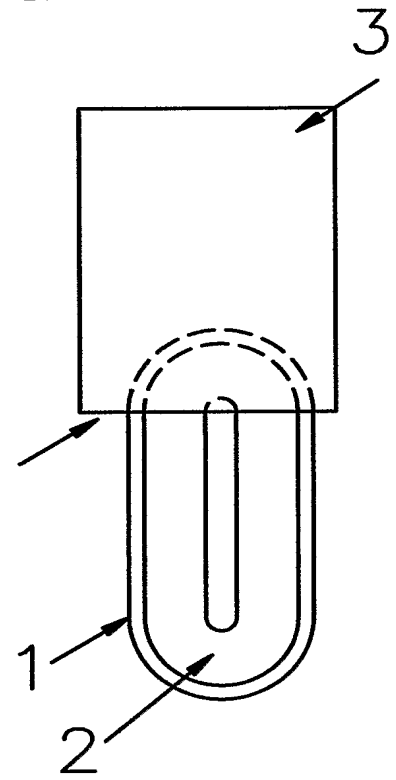


Fig 15e

Fig 15

Fig 16a

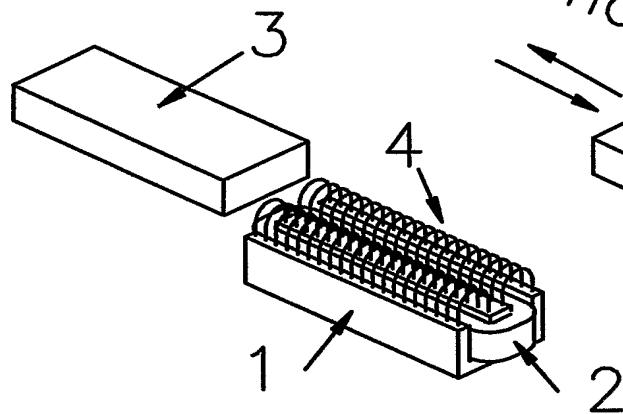
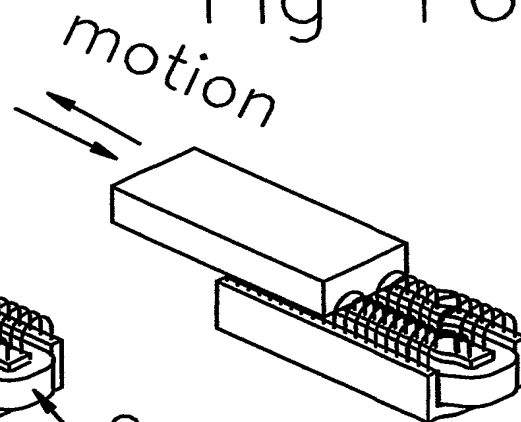
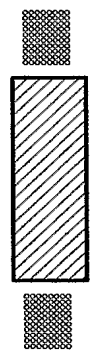


Fig 16b



actuator at position
1 in Fig 31



section DD

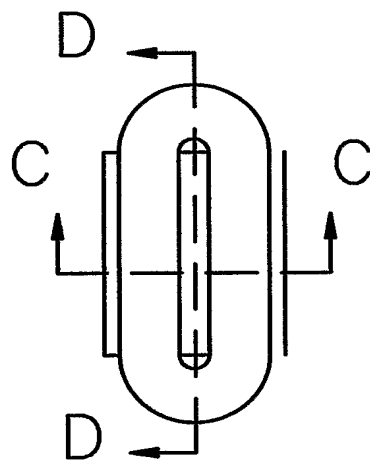


Fig 16c

Fig 16d

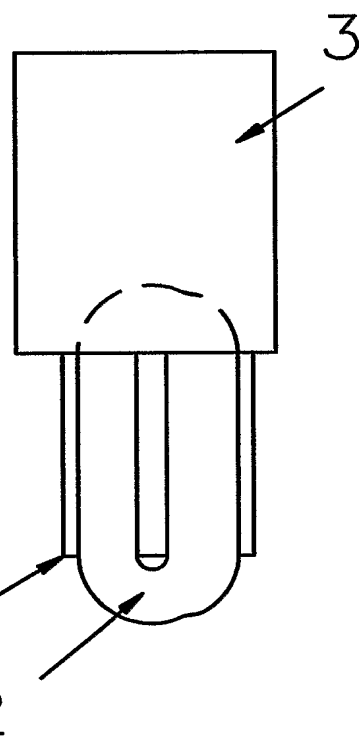


Fig 16e

Fig 16f

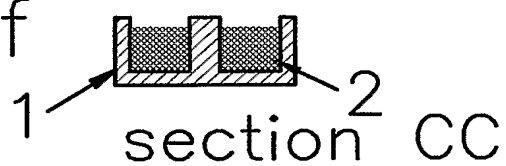


Fig 16

Fig 17a

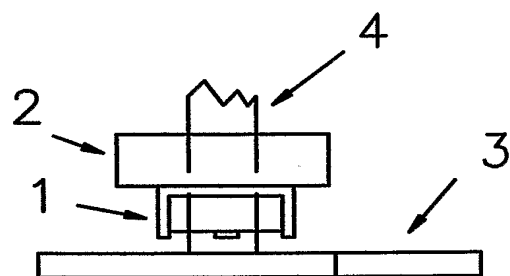


Fig 17b

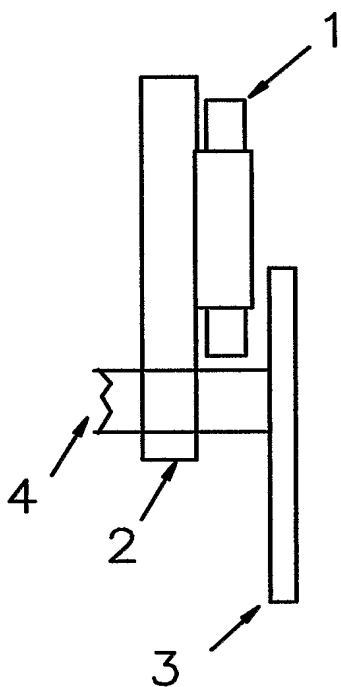
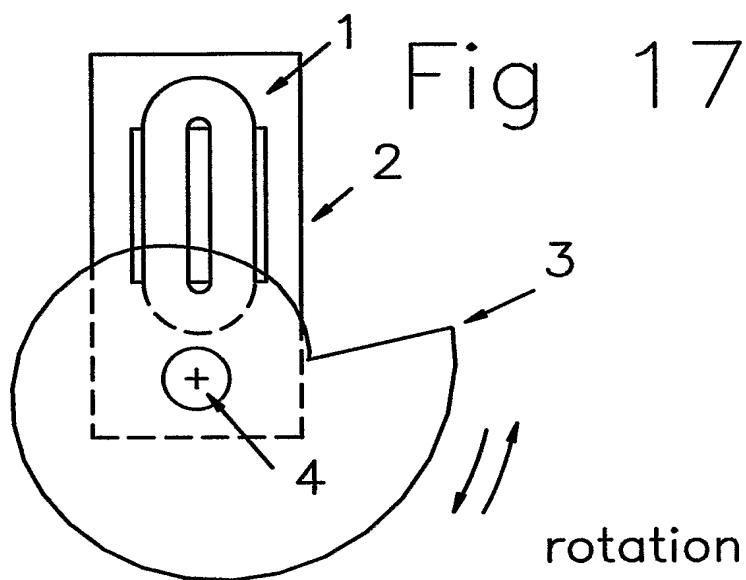


Fig 17c

Fig 17d

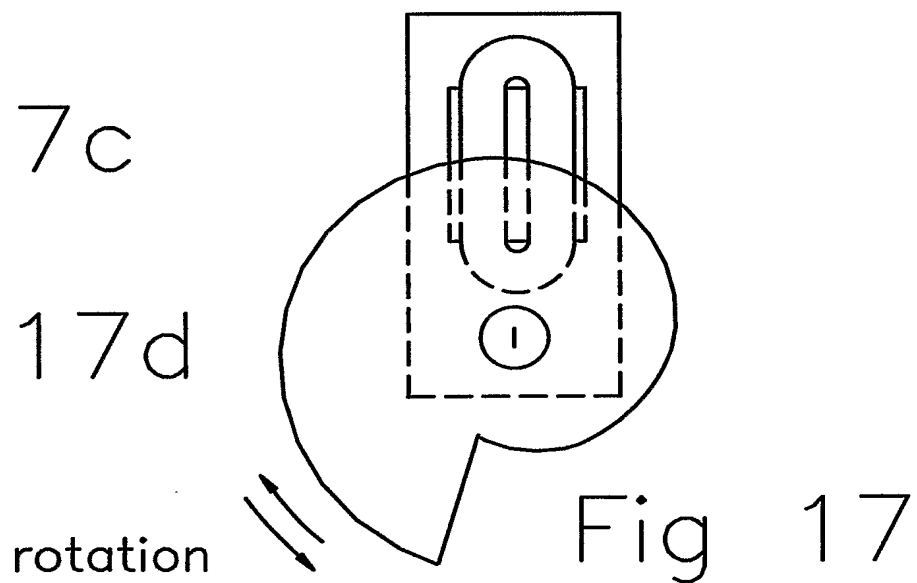


Fig 17

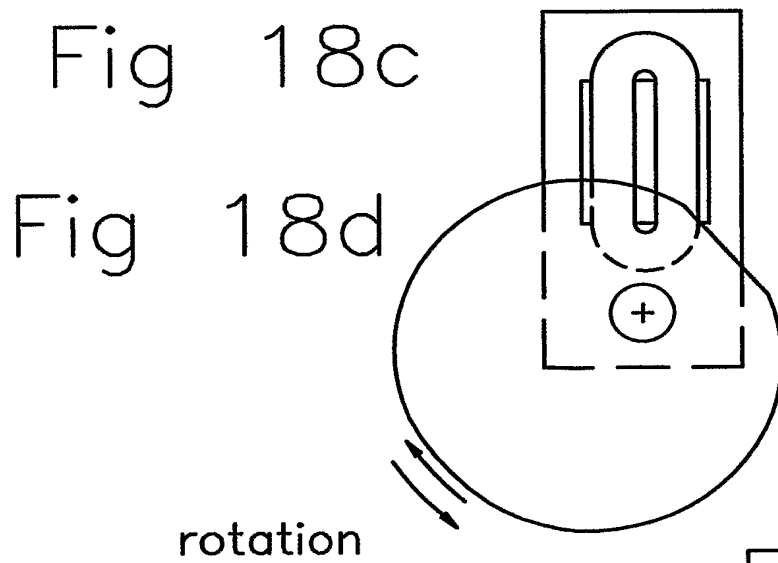
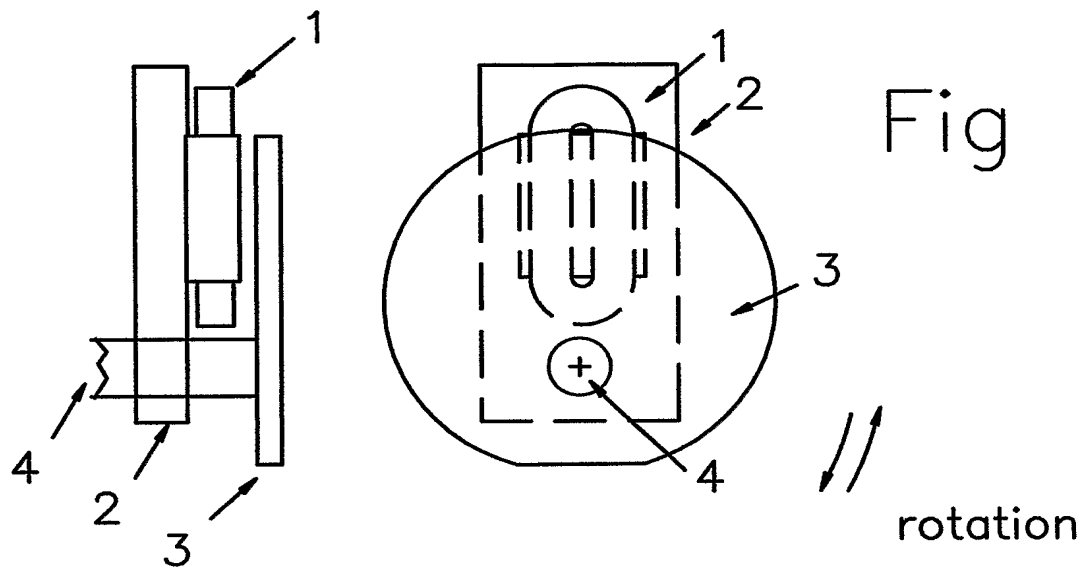
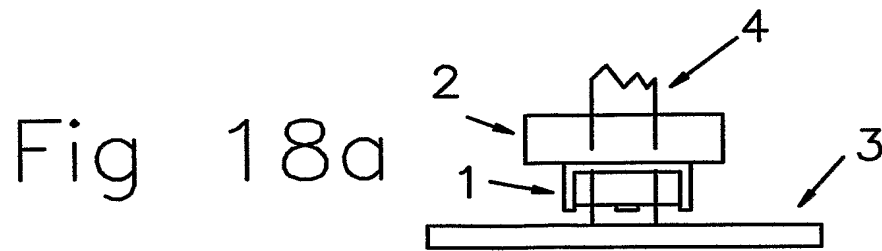


Fig 18

Fig 19a

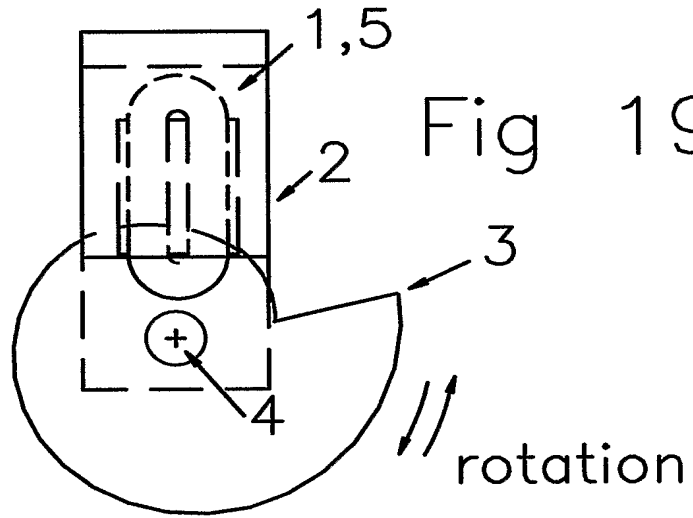
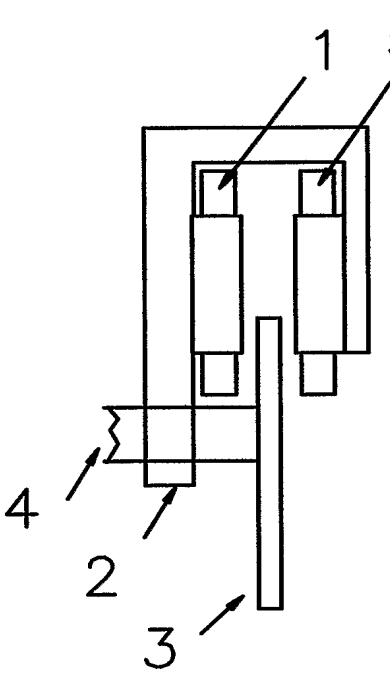
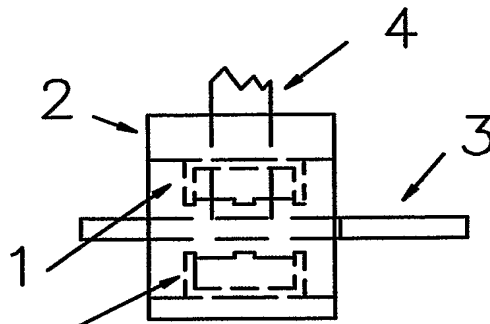


Fig 19b

Fig 19c

Fig 19d

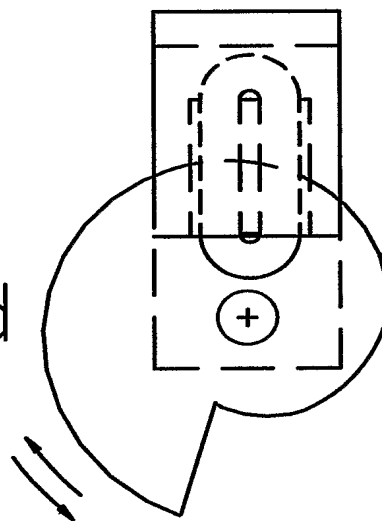


Fig 19

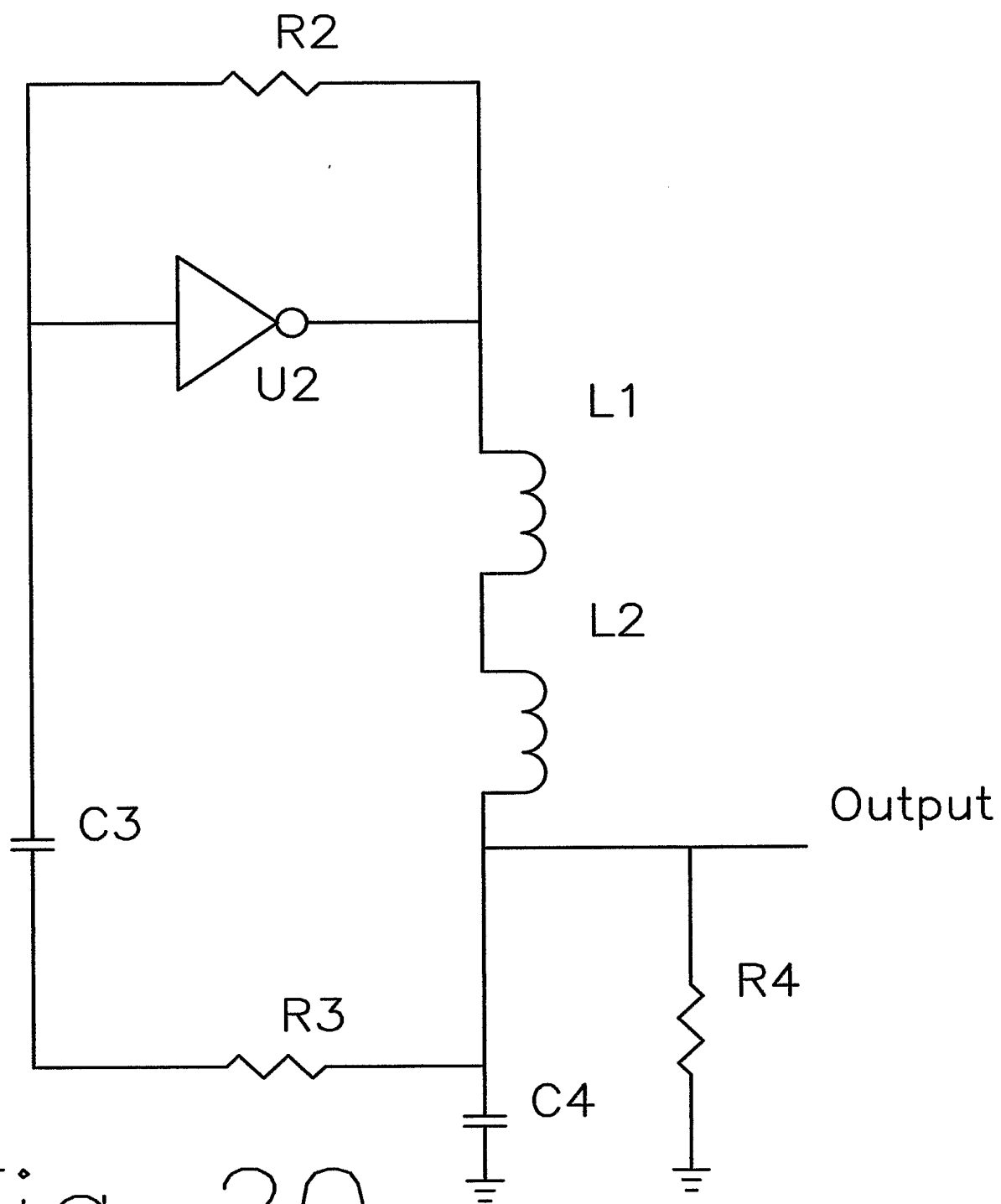


Fig 20

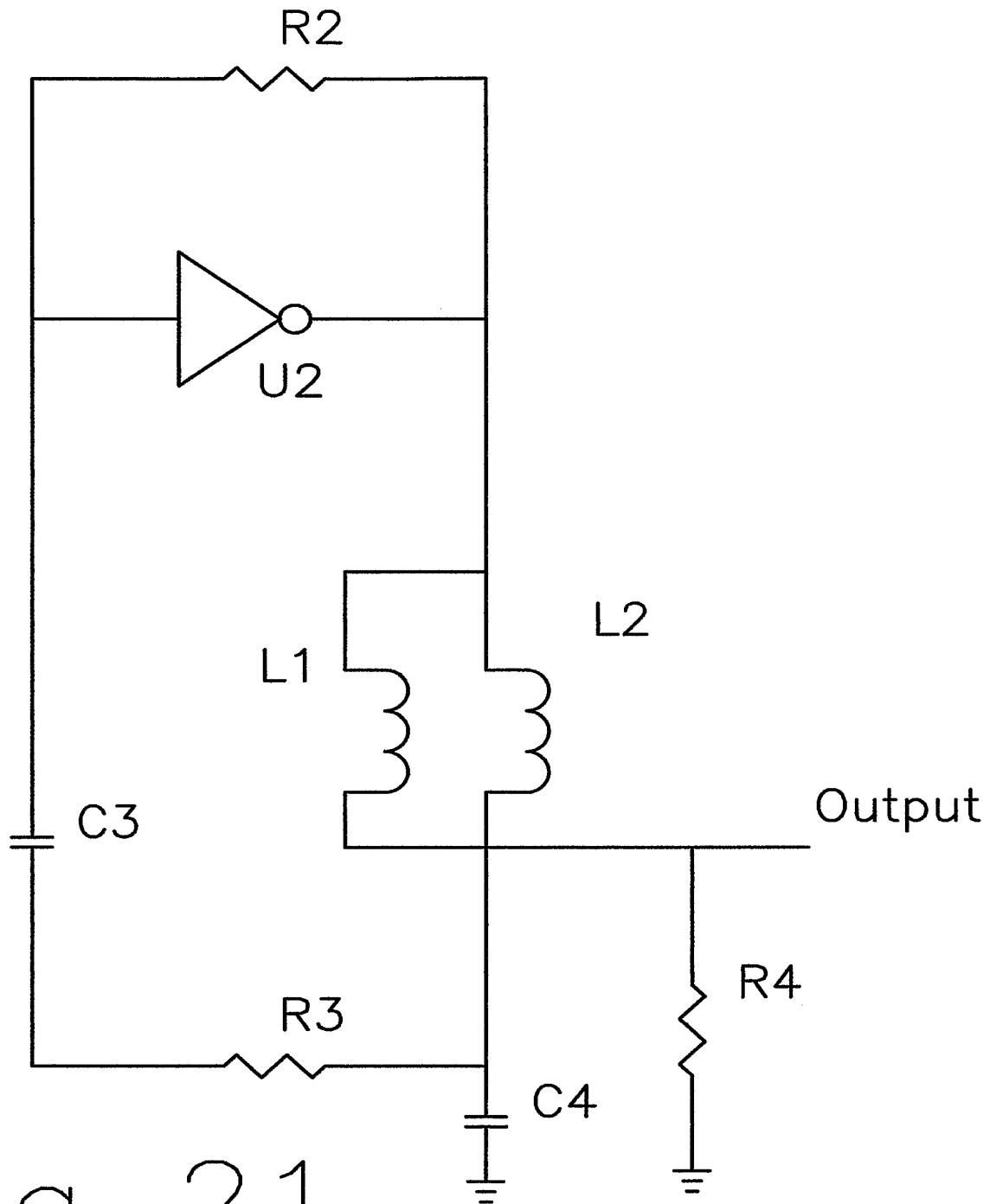


Fig 21

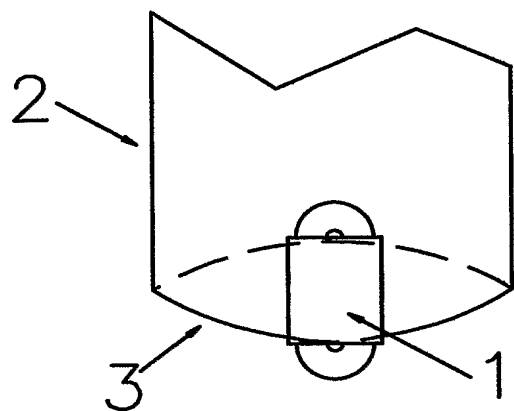


Fig 22a

Fig 22b

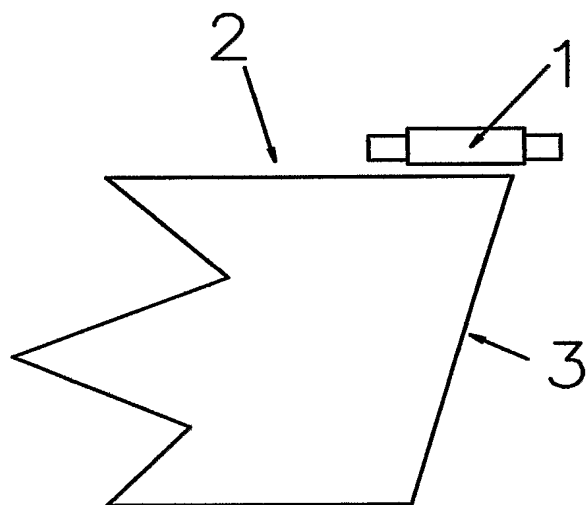
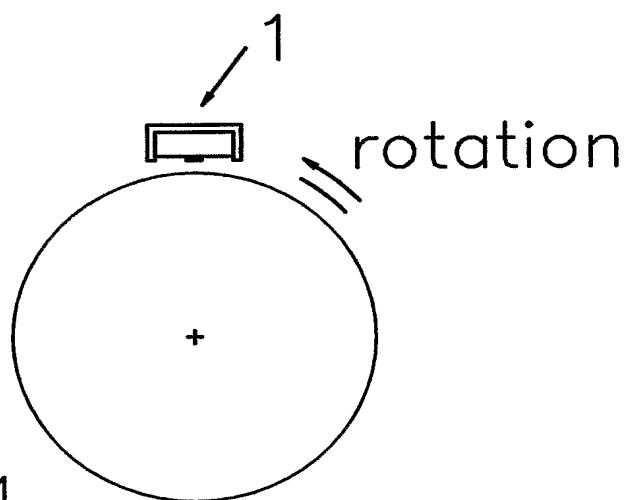


Fig 22c

Fig 22

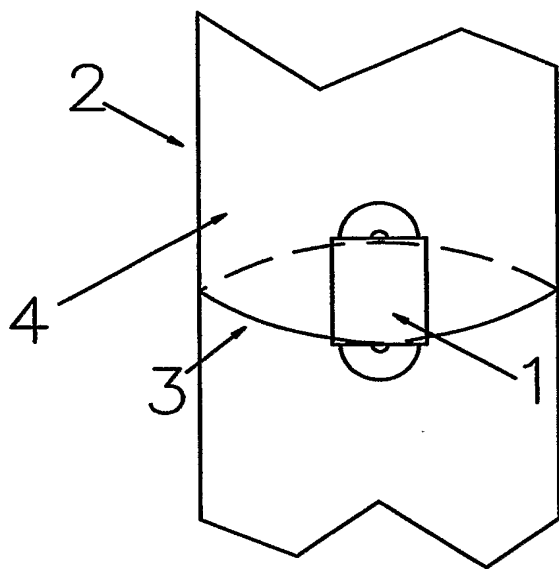


Fig 23a

Fig 23b

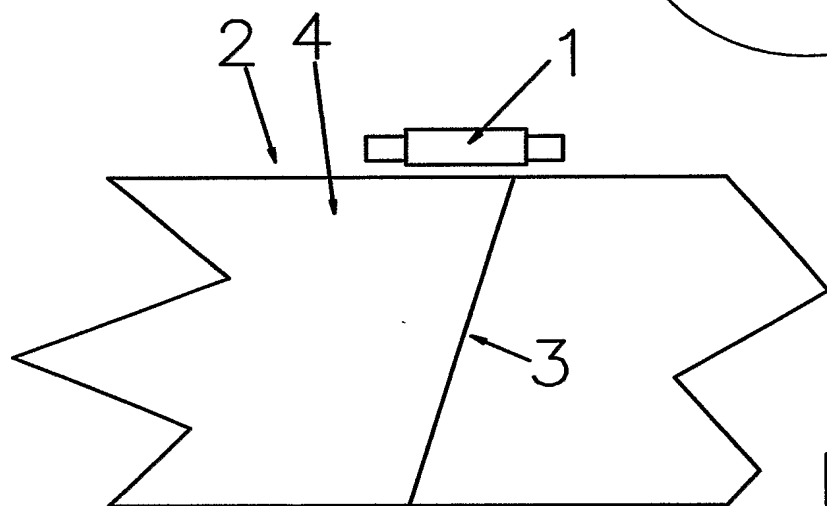
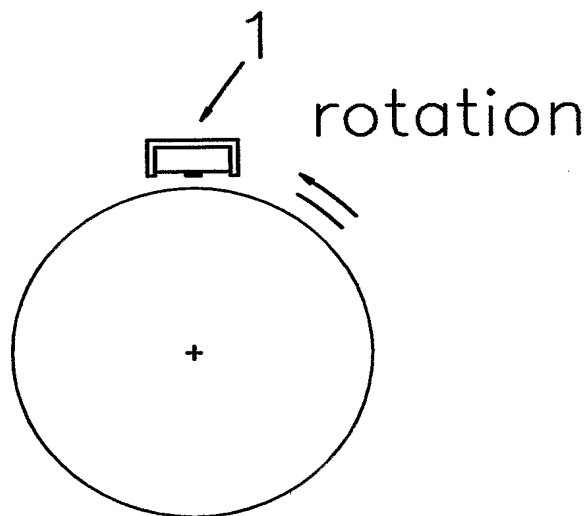


Fig 23c

Fig 23

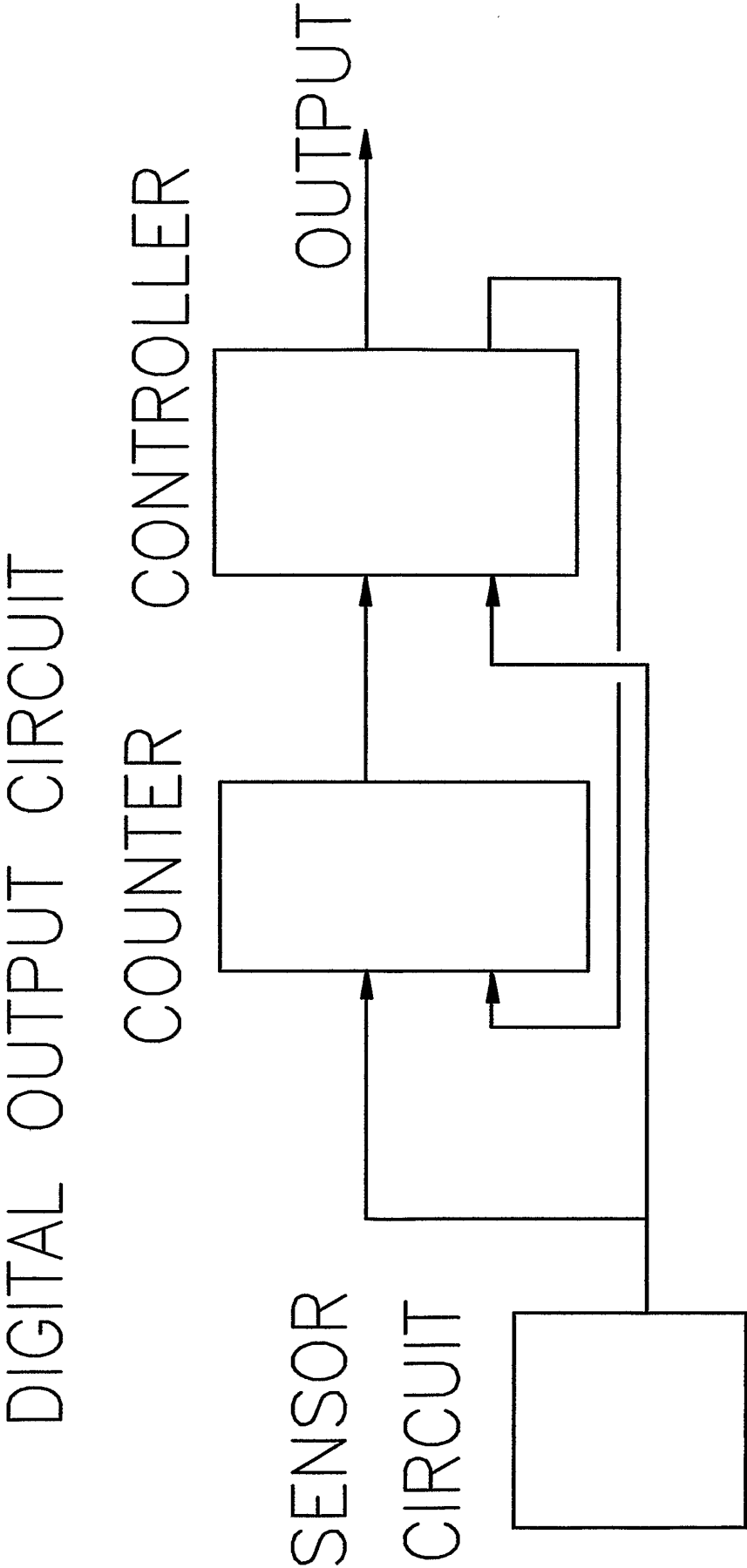


Fig. 24

HIGH SPEED DIGITAL OUTPUT CIRCUIT

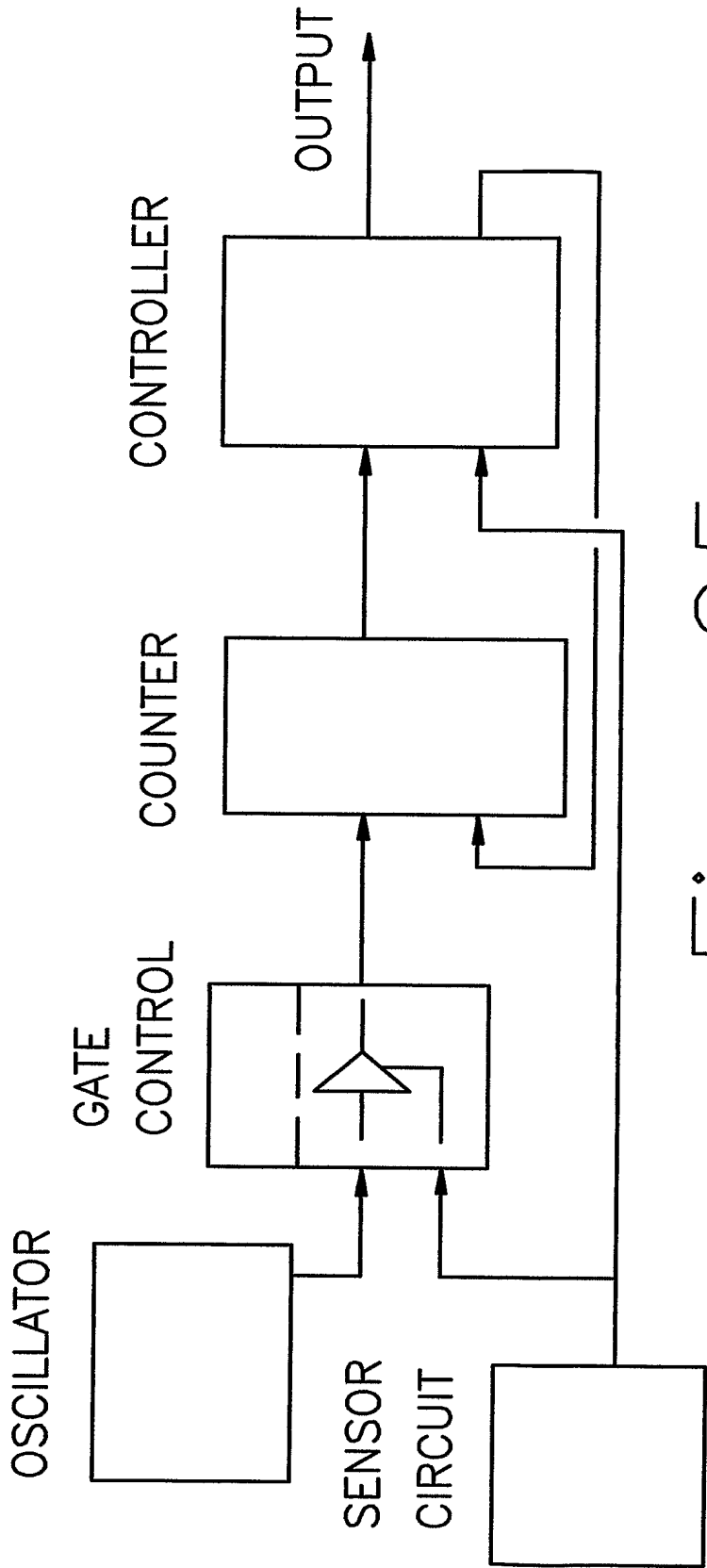


Fig. 25

Fig 26

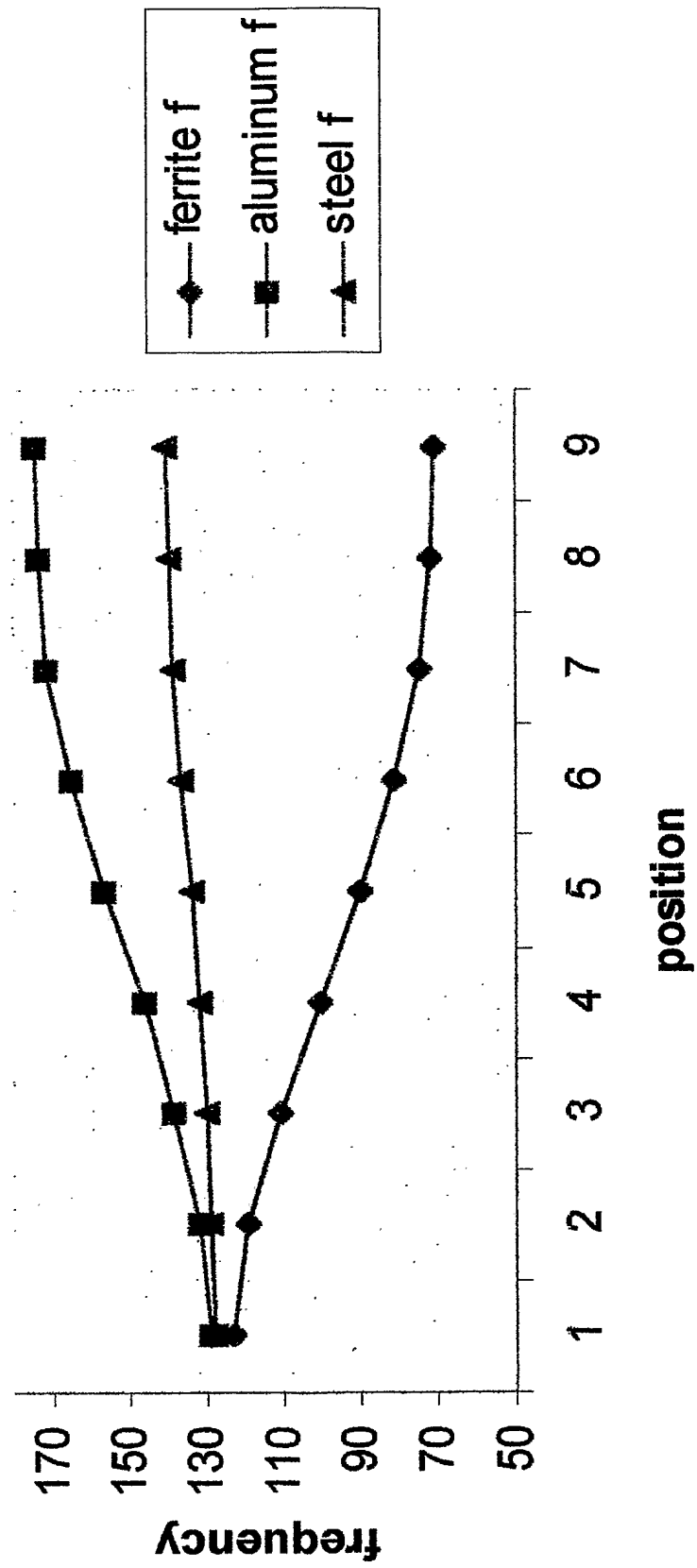


Fig 27

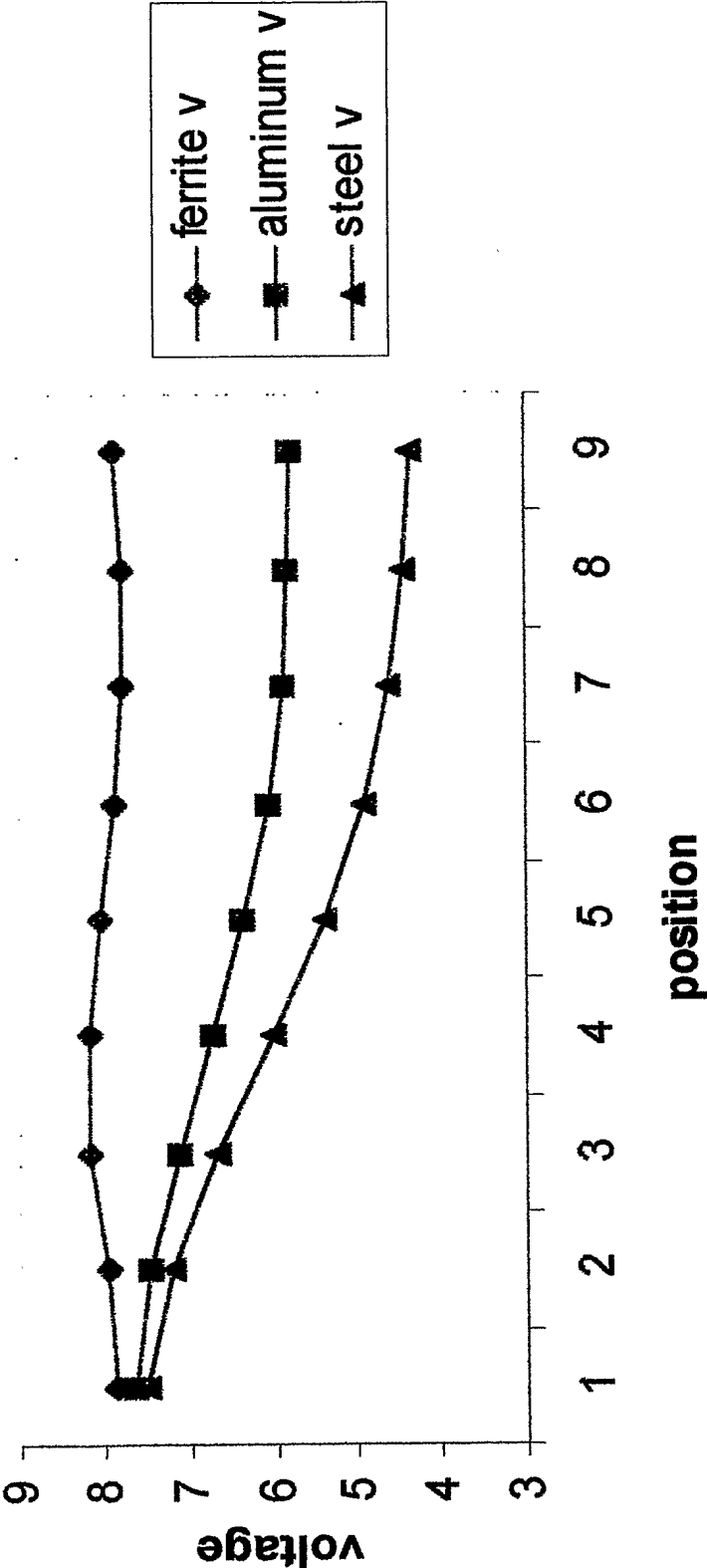


Fig. 28

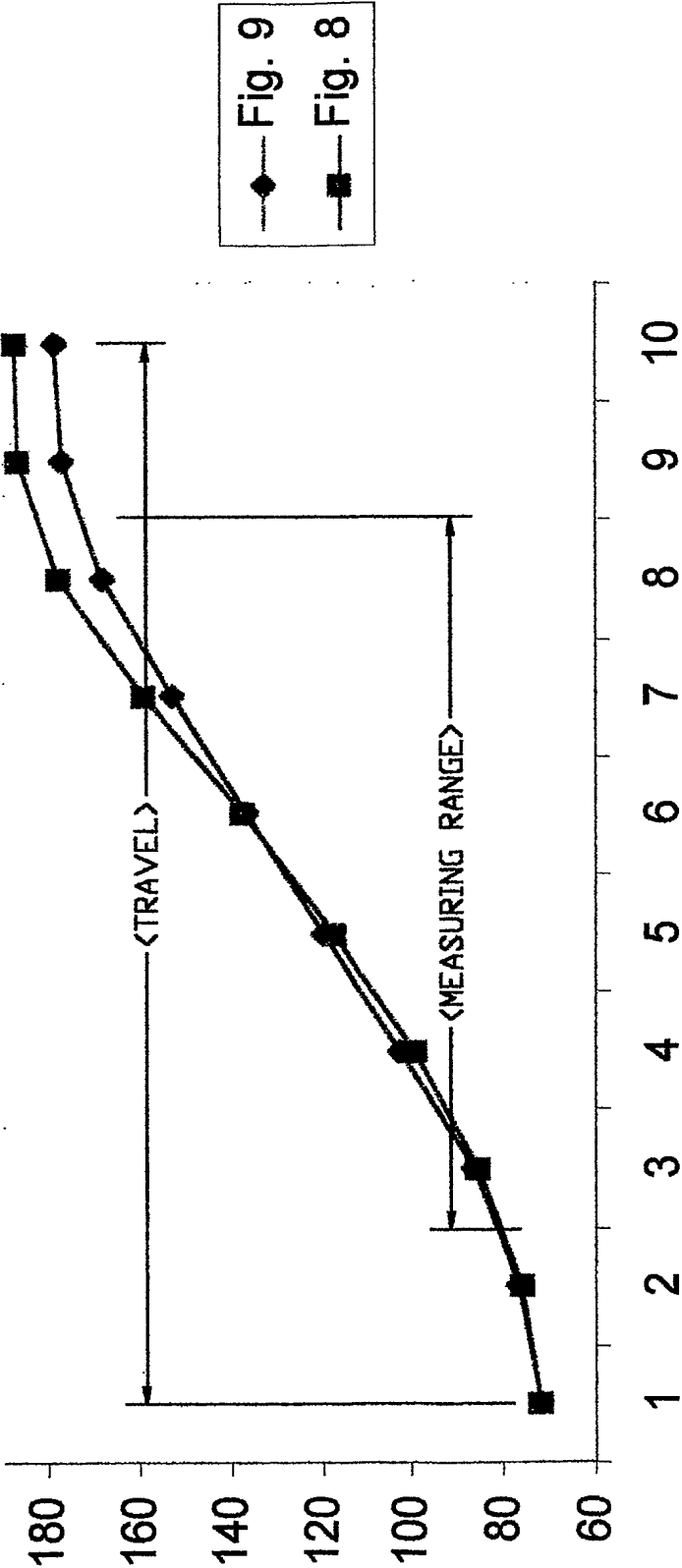


Fig. 29

